



SHEET 1 OF 1

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)		ATTY. DOCKET NO. 061352-0100	SERIAL NO. 10/532,993			
		APPLICANT Michihito UEDA				
		FILING DATE April 28, 2005	GROUP Not yet assigned			
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ₂ (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
	US					
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FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes-Number-Kind Codes (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation Yes No
/JG/		JP 48-102557 A	12/22/1973			partial only
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				
		YAMANAKA et al.; "A CMOS Stochastic Associative Processor Using PWM Chaotic Signals"; IEICE Trans. Electron.; c. 2001; Vol. E84, No. 12; pp. 1723 - 1729				
EXAMINER <i>/James Goodley/</i>			DATE CONSIDERED 03/20/2008			

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EXAMINER
/James Goodley/DATE CONSIDERED
09/18/2008

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